

*Sub  
D1  
Cont  
P1*

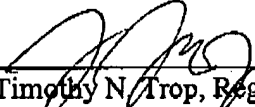
~~a feedback path to provide an output of said pulse generator to said activation circuit, the activation circuit to latch a high signal in response to a low signal on said feedback path.~~

### REMARKS

In a conference by telephone on March 20, 2003, the Examiner indicated that a claim on this form would likely be allowable.

Respectfully submitted,

Date: March 21, 2003

  
\_\_\_\_\_  
Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway, Ste. 100  
Houston, TX 77024  
713/468-8880 [Phone]  
713/468-8883 [Fax]

**APPENDIX**

Please amend claim 11 as follows:

11. (Thrice Amended). An integrated circuit comprising:

an activation circuit to determine whether a supply voltage reaches a predetermined level, said activation circuit including an inverter coupled to the gate of a load transistor, a second transistor coupled to said load transistor and a third transistor coupled between said load transistor and said first transistor;

a pulse generator to generate pulses to indicate that a supply voltage is ramping up and to terminate the generation of the pulses after the supply voltage reaches a predetermined level; and

a feedback path to provide an output of said pulse generator to said activation circuit, [said feedback path including an inverter to create] the activation circuit to latch a high signal in response to a low signal on said feedback path.